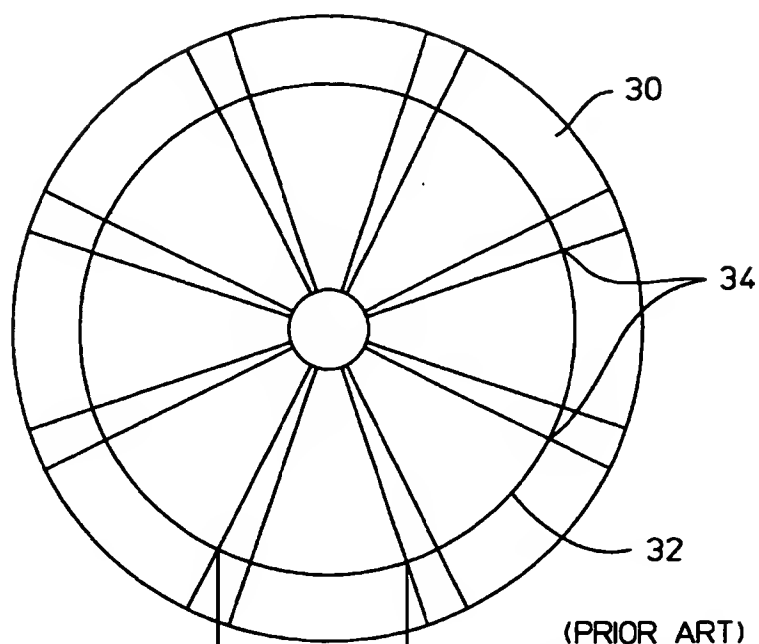


FIG. 1a (PRIOR ART)



(PRIOR ART)
Figure 3a

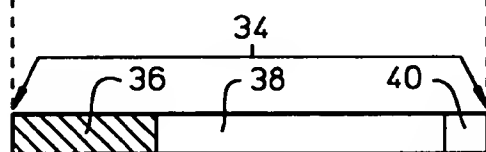


Figure 3b (PRIOR ART)

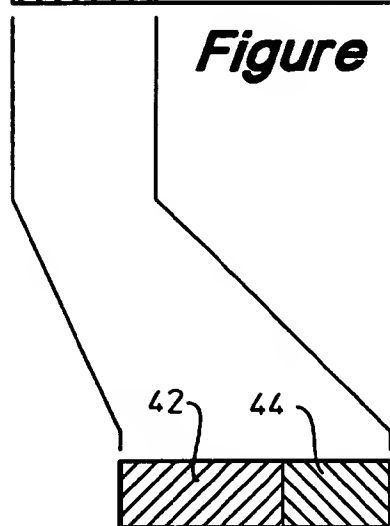


Figure 3c (PRIOR ART)

05997036-1394
FIG. 3a

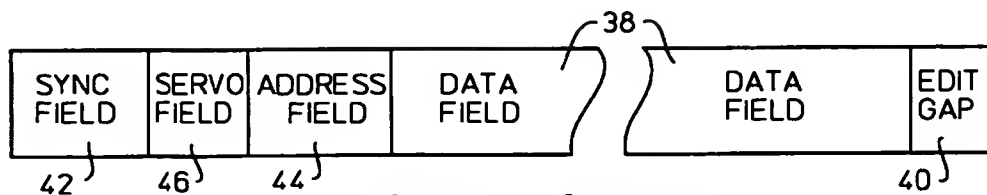


Figure 4a (PRIOR ART)

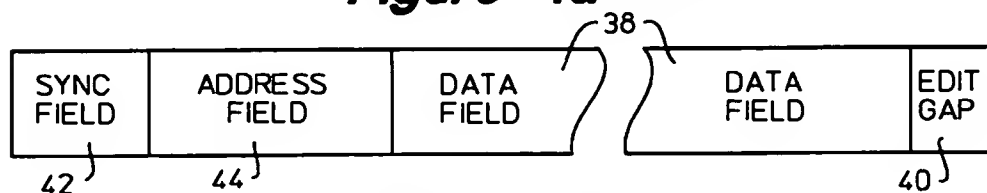


Figure 4b (PRIOR ART)

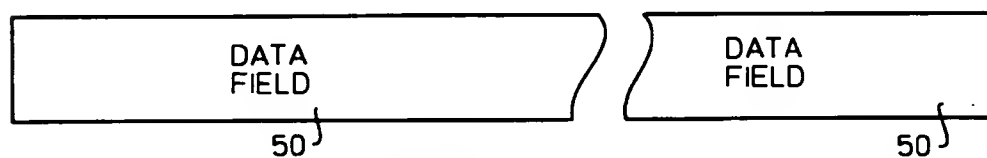


Figure 4c (PRIOR ART)



Figure 5a

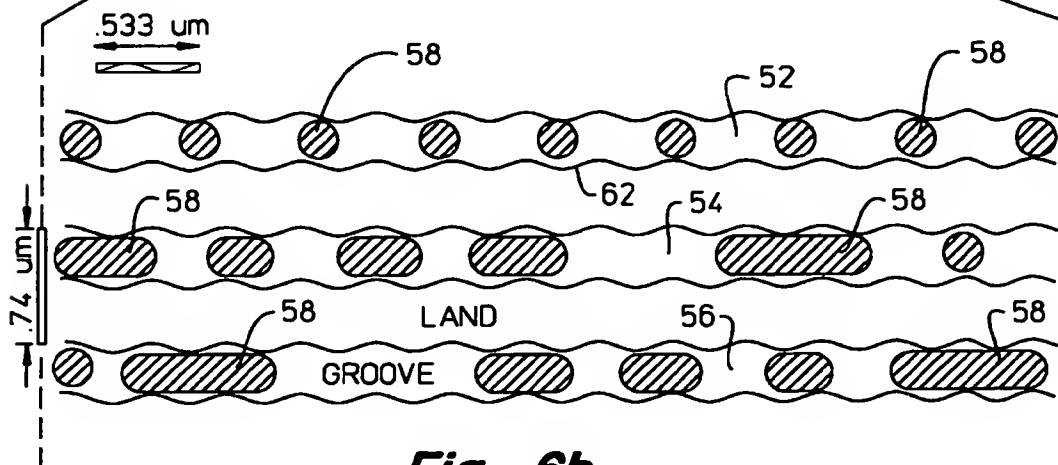
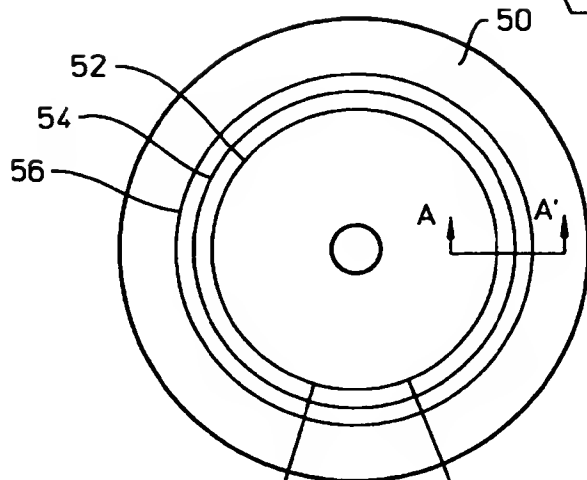
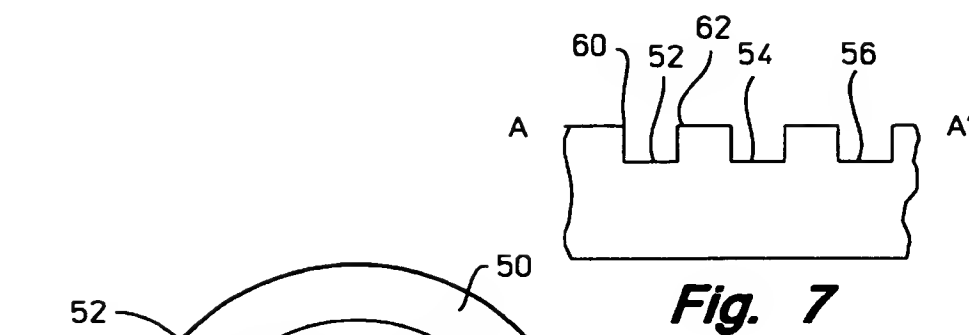


Figure 5b



Figure 5c

0597036-42701



09997036-112704

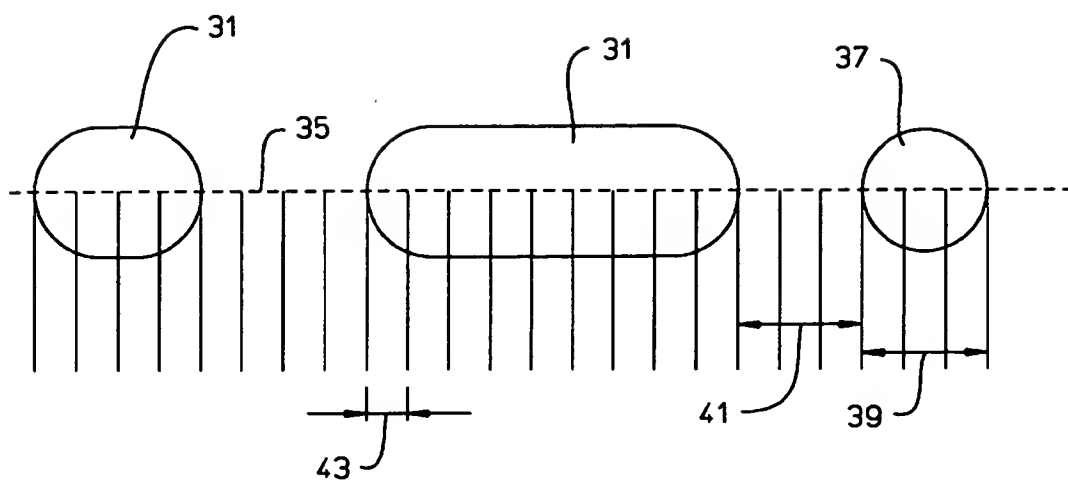


Fig. 8

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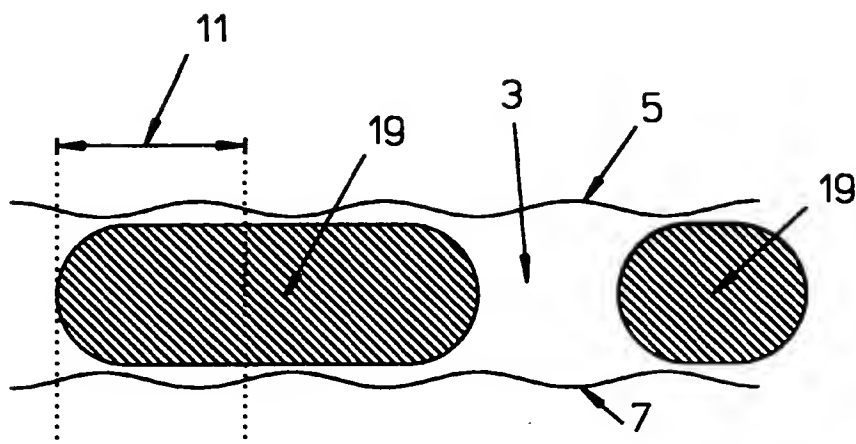


Fig. 9a

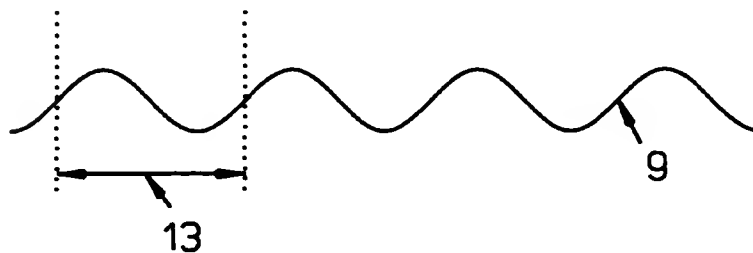


Fig. 9b

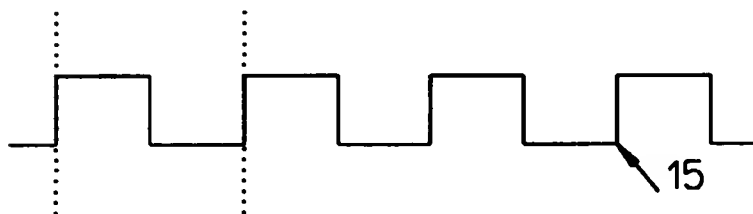


Fig. 9c

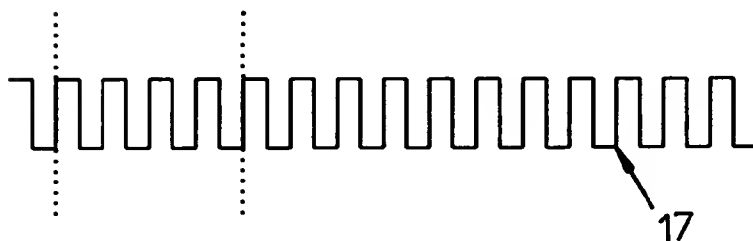


Fig. 9d

09997036-112701

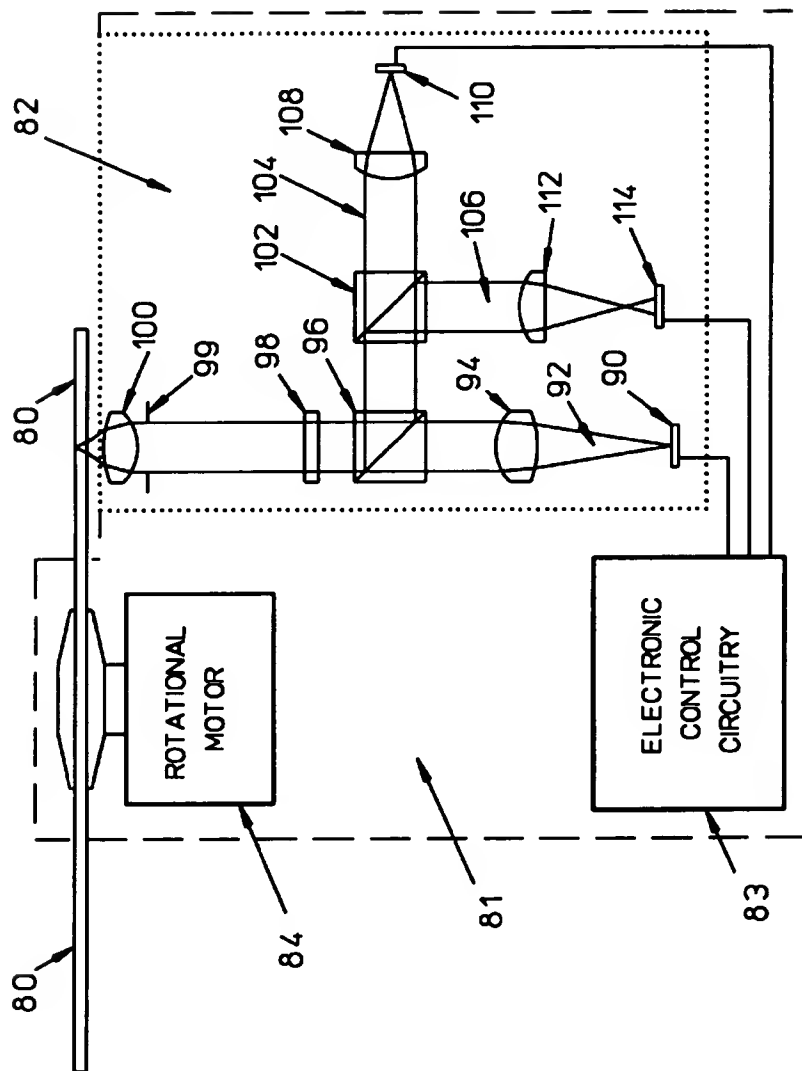


Fig. 10

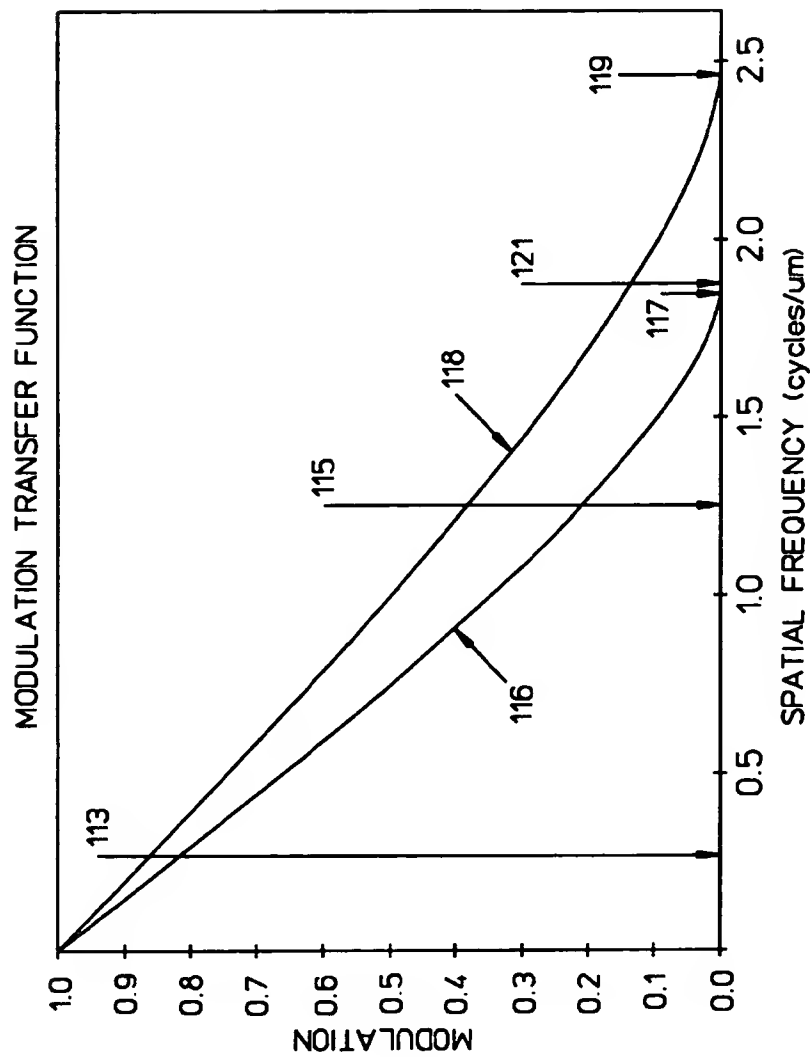


Fig. 11

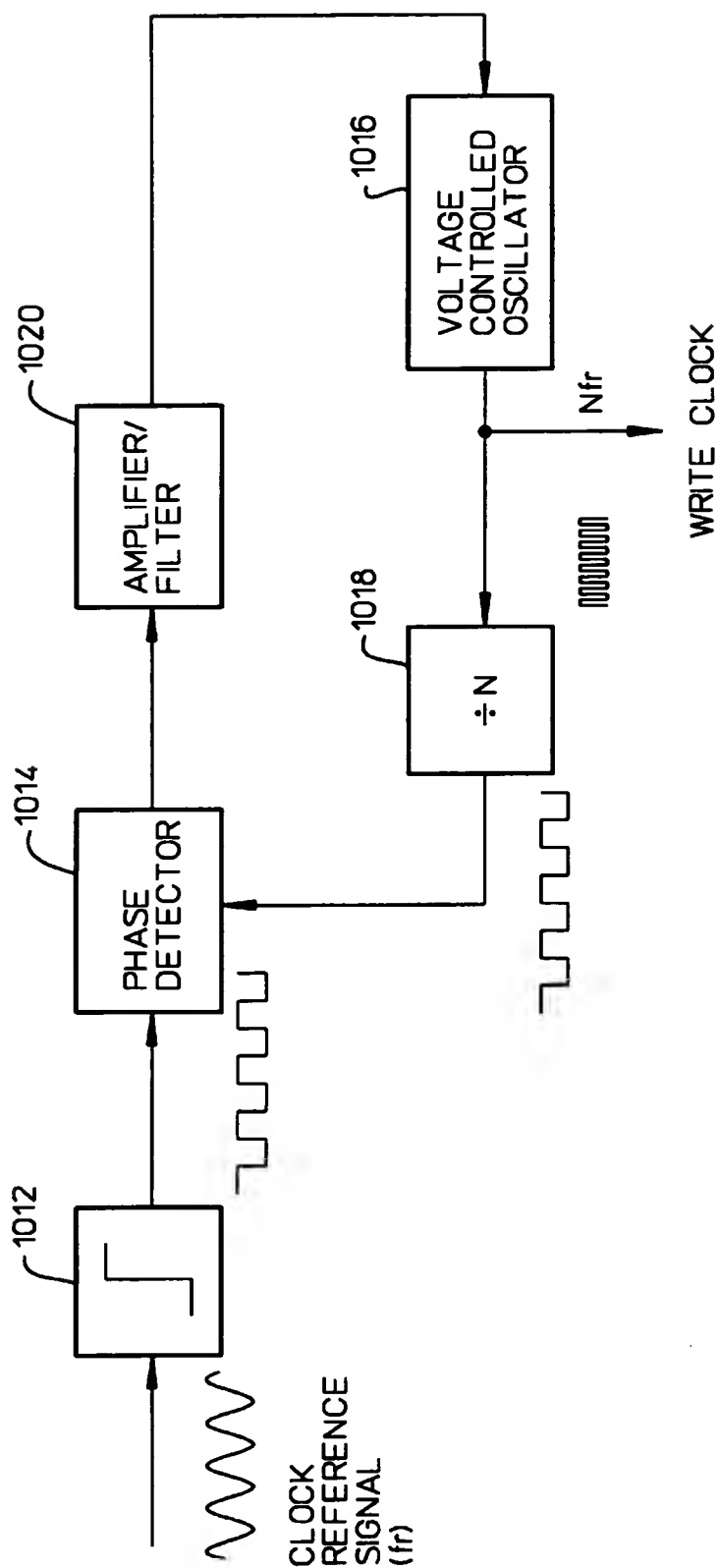


Fig. 12

FIG. 12 is a block diagram of a PLL system.

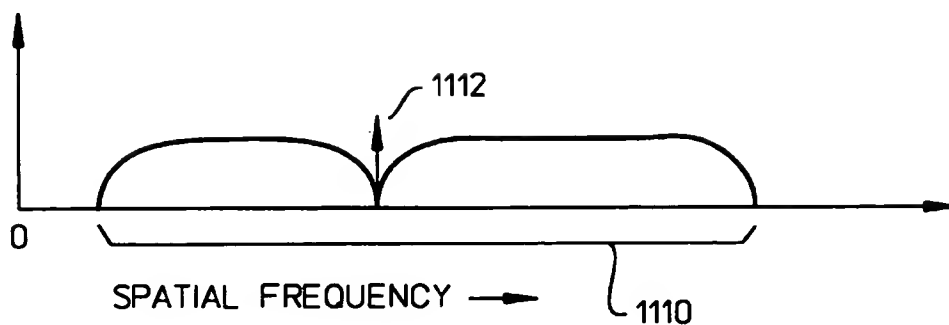


Fig. 13

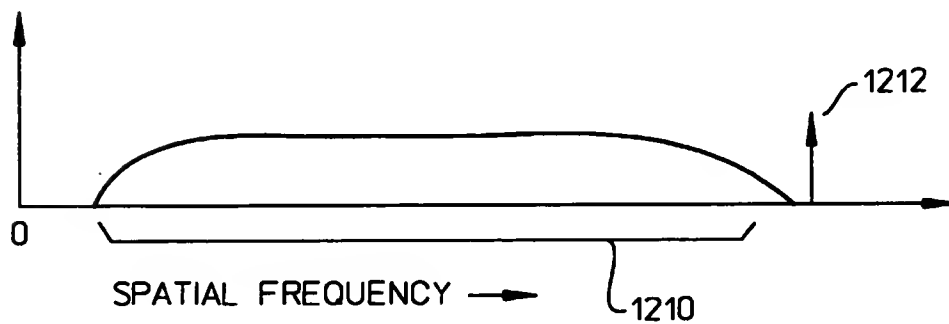


Fig. 14

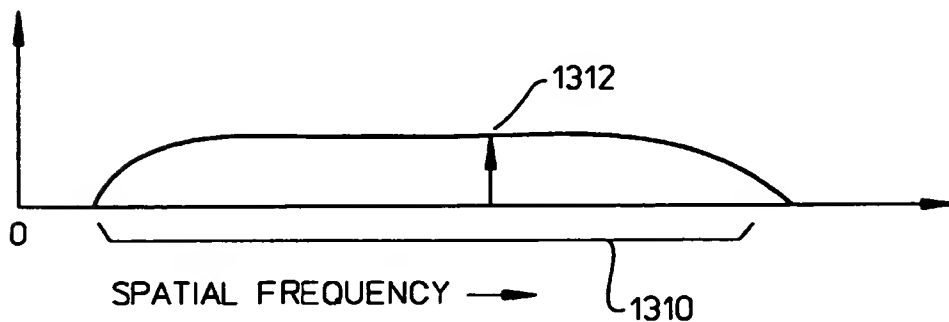


Fig. 15

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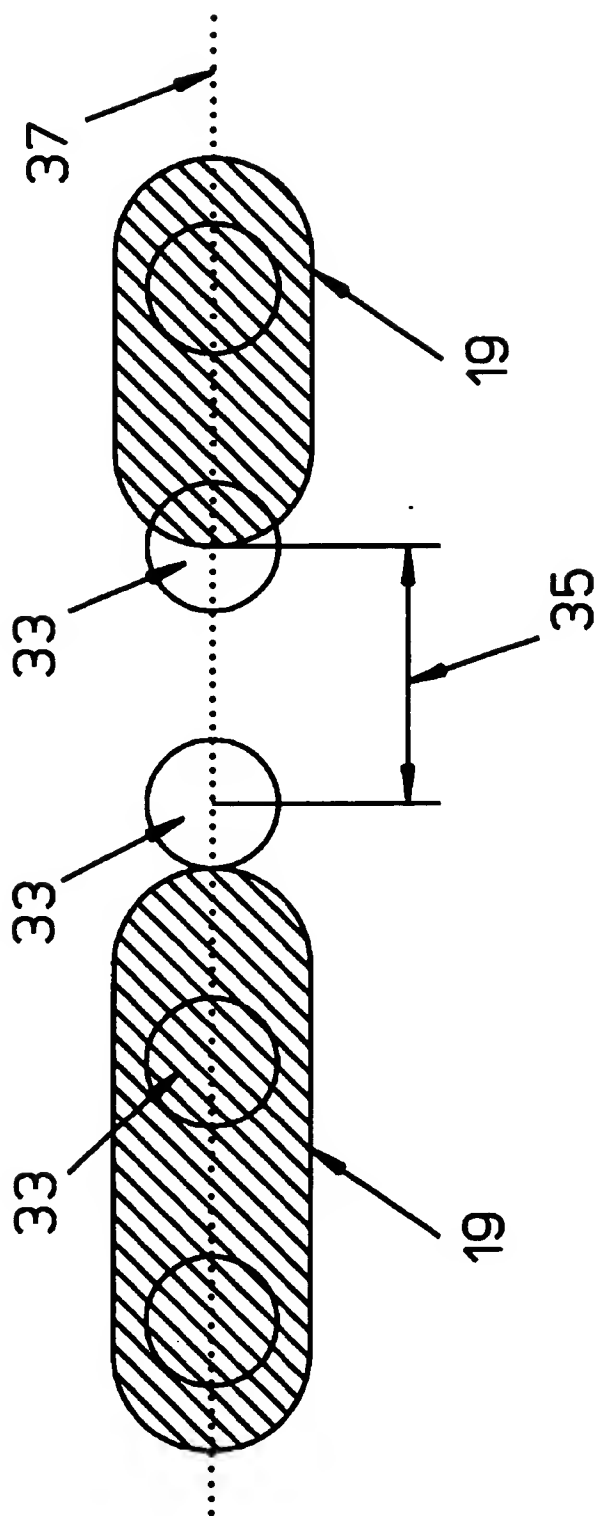


Fig. 16

FO/2217-9302550

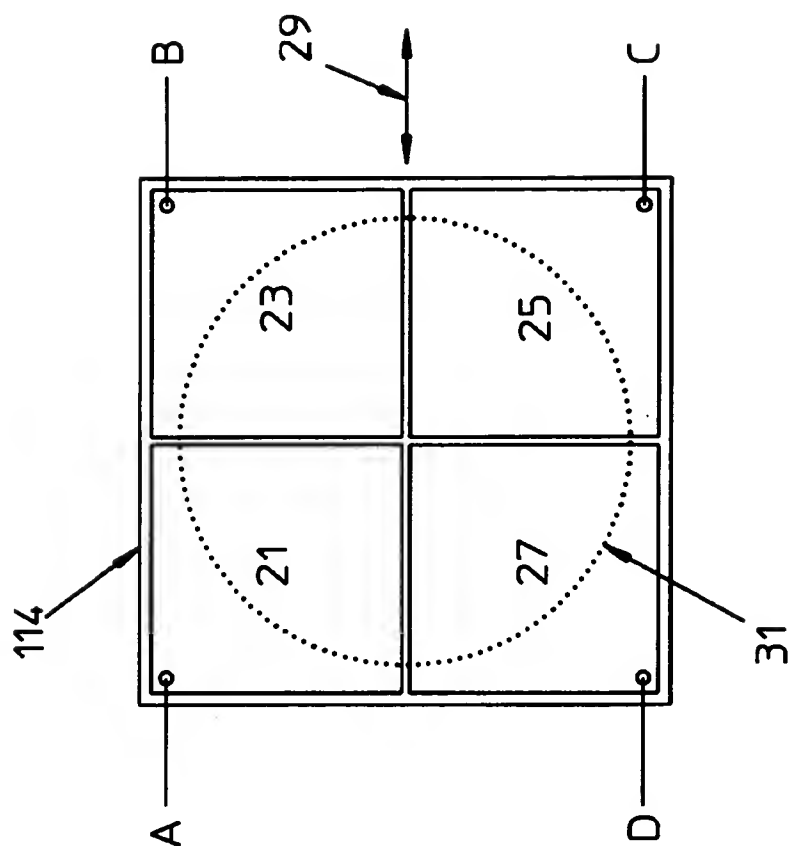


Fig. 17

FOI b7 - 9602650

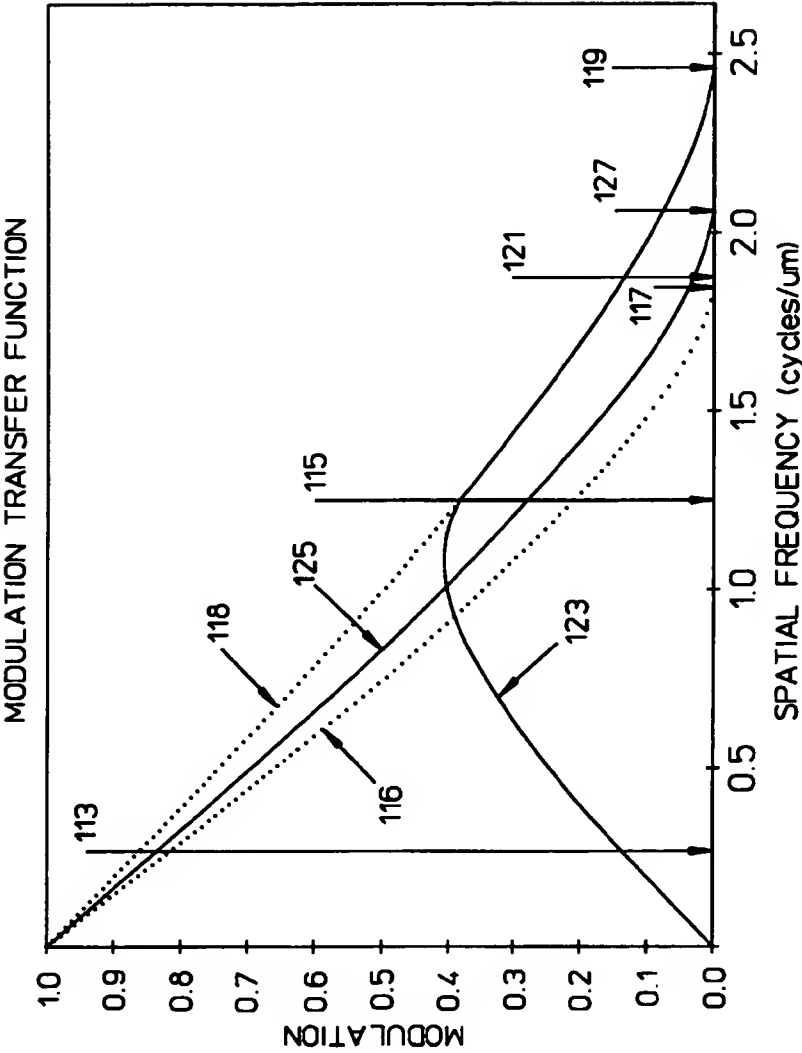


Fig. 18

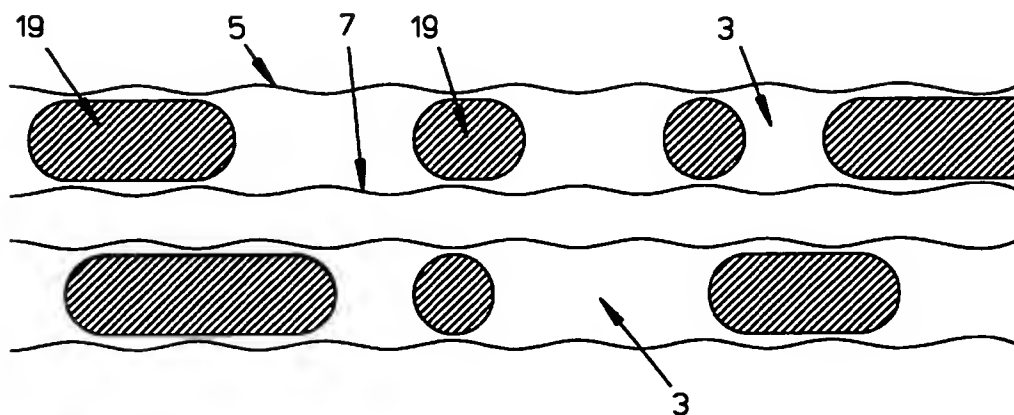


Fig. 19

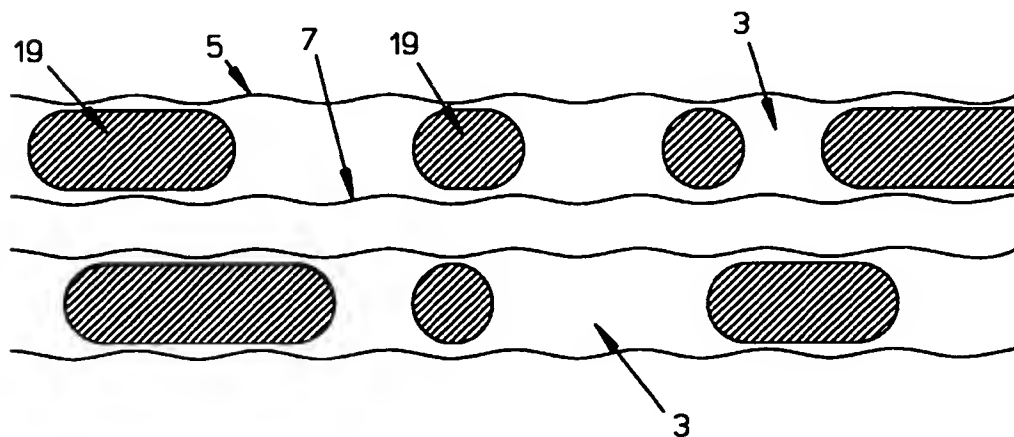


Fig. 20

09597036.112701

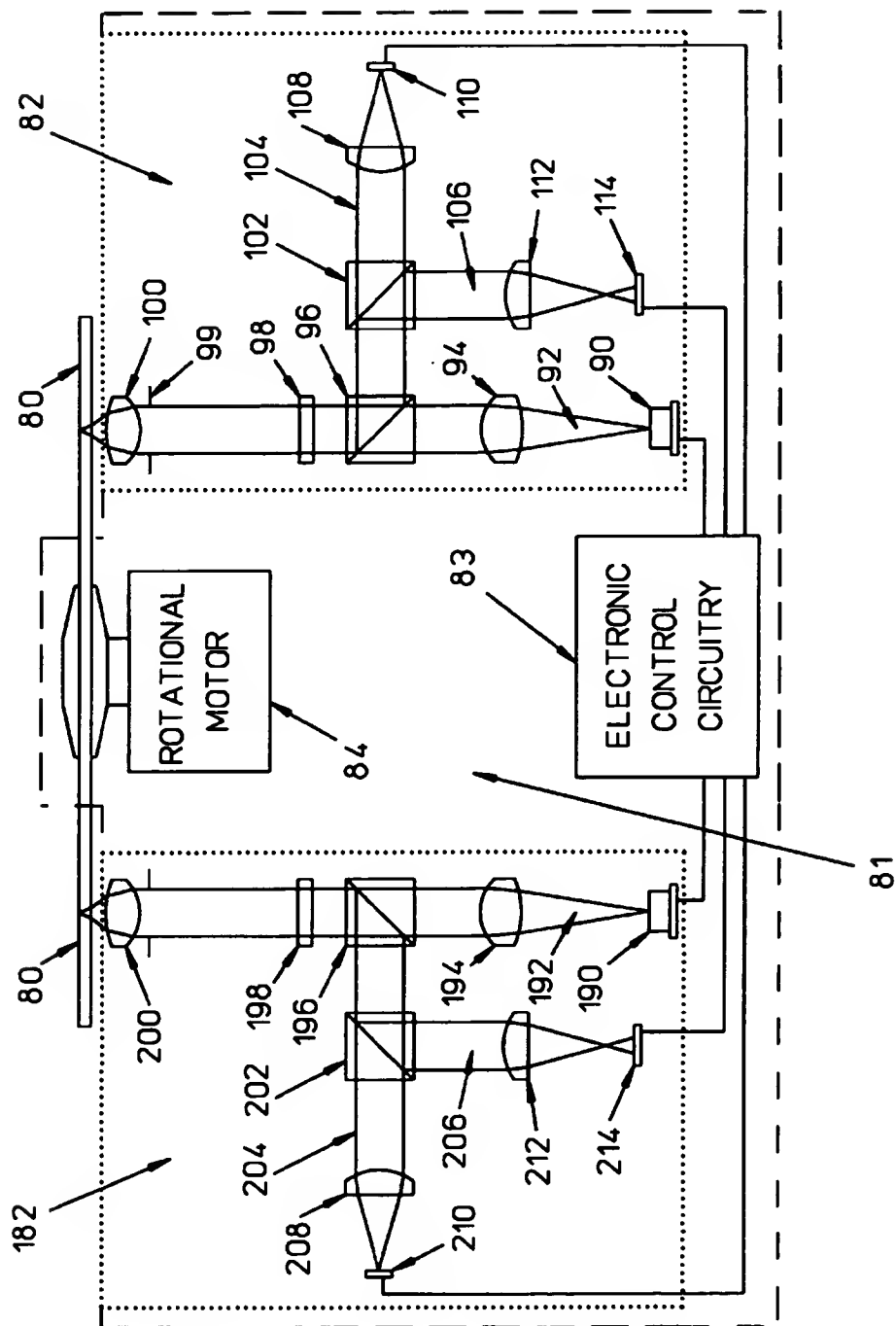


Fig. 21

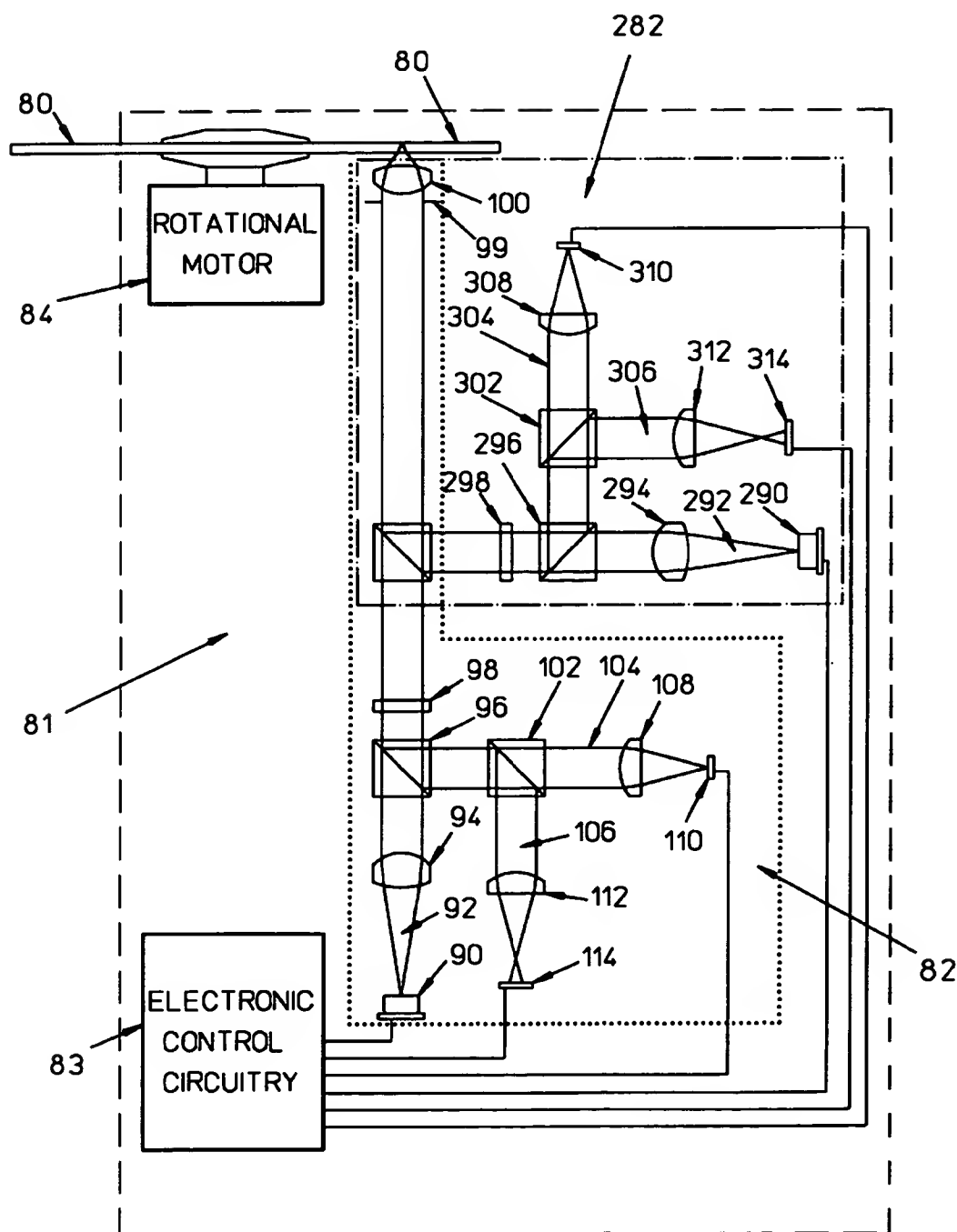


Fig. 22